

Specification

Small Form Factor Pluggable

Duplex LC Receptacle – SFP+

Optical Transceivers

10 Gigabit Ethernet

10GBASE-LR Lite



Ordering Information

TAS-A1NA1-D11

Model Name	Voltage	Category	Device type	Interface	LOS	Temperature	Distance	Latch Color
TAS-A1NA1-D11	3.3V	With DDMI	1310 nm FP	AC / AC Coupling	LVTTL	0°C ~ +70°C	2km	Blue

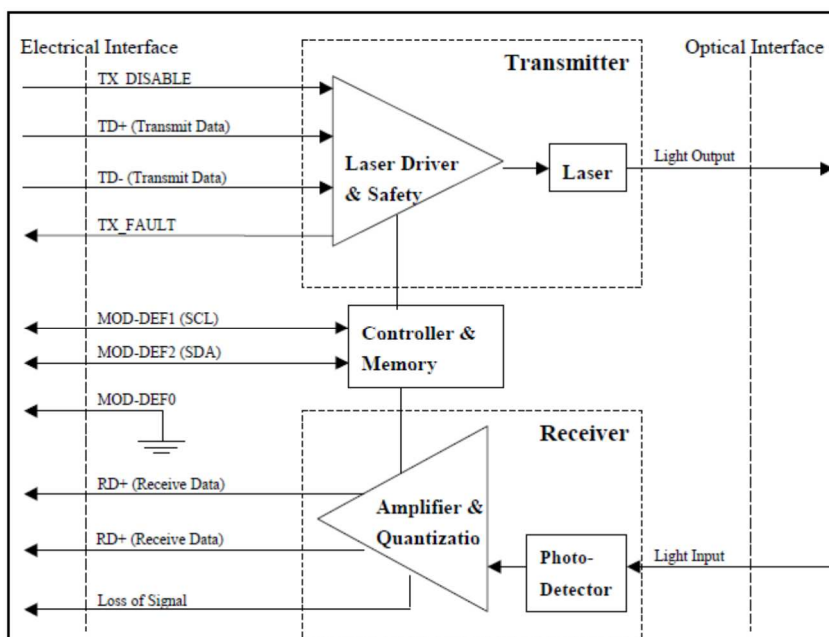
Features

- Optical Interface compliant to IEEE 802.3ae 10GBASE-LR
- Compliant with SFP+ MSA
- Data Rate 10.3125Gbps
- 1310nm FP TOSA and PIN ROSA
- Maximum link length of 2km SMF
- LC duplex receptacle
- Low power dissipation (< 1W)
- Hot Pluggable
- All-metal housing for superior EMI performance
- Built in digital diagnostic Functions
- Operating case temperature range: Commercial Temperature 0°C ~ 70°C
- ROHS Compliant

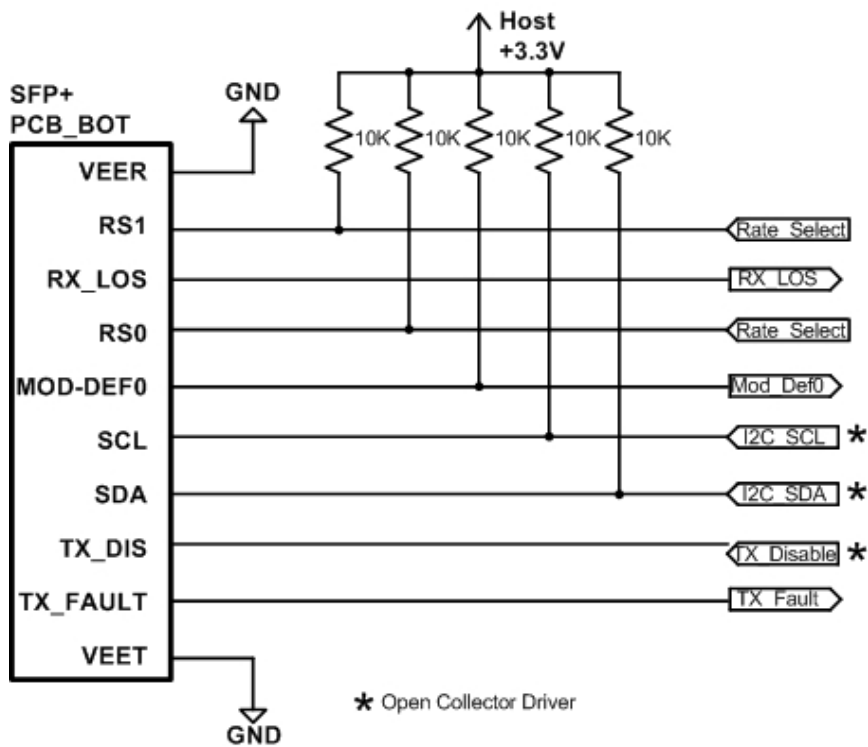
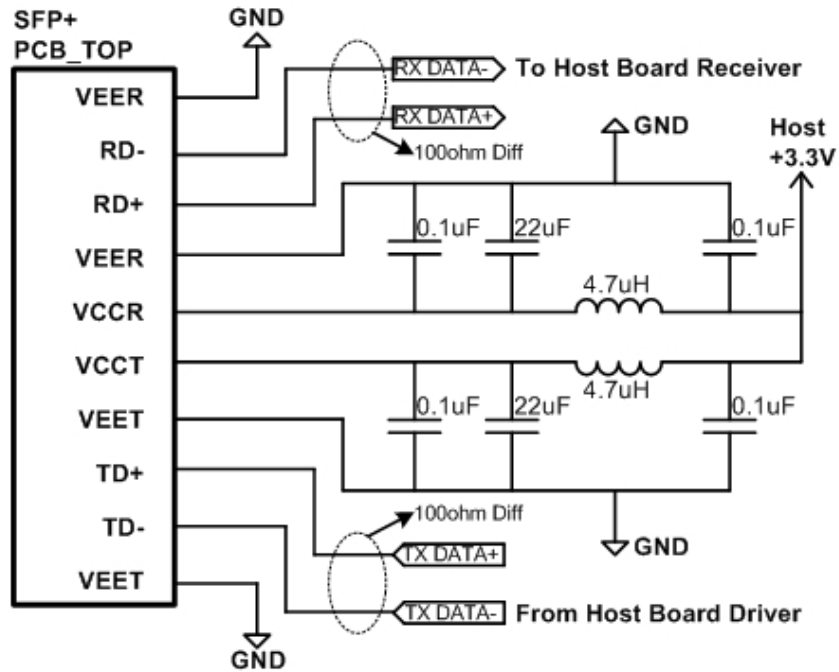
Applications

- 10GBASE-LR 10Gigabit Ethernet up to 2KM

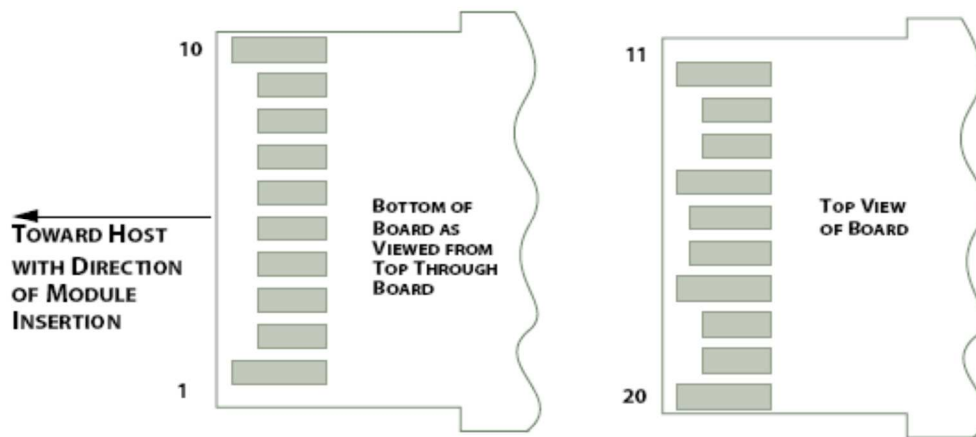
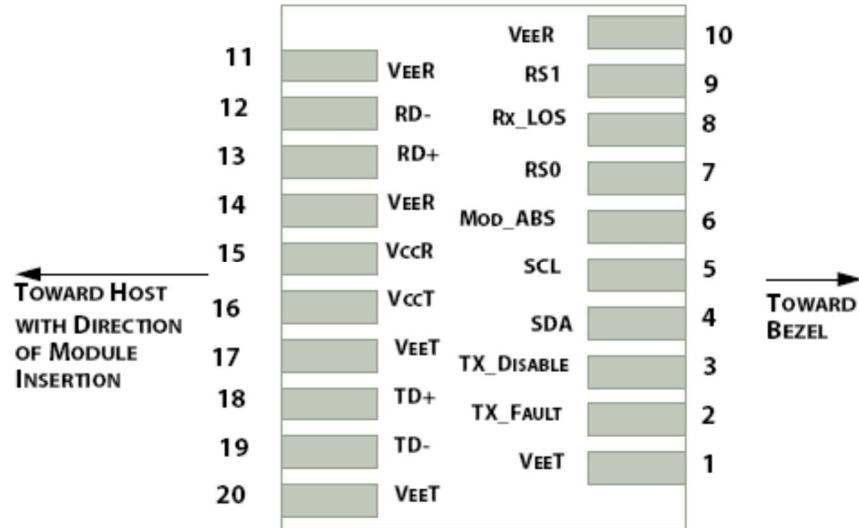
Transceiver Block Diagram



Proposed Applications Schematics



Pin Definition and Descriptions



PIN	Logic	Symbol	Name / Description	Note
1		VeeT	Module Transmitter Ground	1
2	LVTTL-O	TX_Fault	Module Transmitter Fault	2
3	LVTTL-I	TX_Dis	Transmitter Disable; Turns off transmitter laser output	3
4	LVTTL-I/O	SDA	2-Wire Serial Interface Data Line	
5	LVTTL-I	SCL	2-Wire Serial Interface Clock	
6		Mod_ABS	Module Absent, connected to VeeT or VeeR in the module	
7	LVTTL-I	RS0	Rate Select 0, optionally controls SFP+ module receiver. When high input signaling rate > 4.25 GBd and when low input signal rate \leq 4.25 GBd.	
8	LVTTL-O	RX_LOS	Receiver Loss of Signal Indication	
9	LVTTL-I	RS1	Rate Select 1, optionally controls SFP+ module transmitter. When high input signaling rate > 4.25 GBd and when low input signal rate \leq 4.25 GBd.	
10		VeeR	Module Receiver Ground	1
11		VeeR	Module Receiver Ground	1
12	CML-O	RD-	Receiver Inverted Data Output	
13	CML-O	RD+	Receiver Data Output	
14		VeeR	Module Receiver Ground	1
15		VccR	Module Receiver 3.3 V Supply	
16		VccT	Module Transmitter 3.3 V Supply	
17		VeeT	Module Transmitter Ground	1
18	CML-I	TD+	Transmitter Non-Inverted Data Input	
19	CML-I	TD-	Transmitter Inverted Data Input	
20		VeeT	Module Transmitter Ground	1

Note:

1. Module ground pins are isolated from the module case and chassis ground within the module.
2. Shall be pulled up with 4.7k to 10k ohm to a voltage between 3.15V and 3.45V on the host board.
3. Shall be pulled up with 4.7k to 10k ohm to VccT in the module.

Absolute Maximum Ratings

Parameters	Symbol	Min.	Max.	Unit
Power Supply Voltage	V _{CC}	0	3.6	V
Storage Temperature	T _S	-40	85	°C
Relative Humidity	RH	5	95	%

Recommended Operating Environment

Parameters	Symbol	Min.	Typical	Max	Unit
Power Supply Voltage	V _{CC}	3.135	3.3	3.465	V
Operating Case Temperature	T _{op}	0	25	70	°C
Power Supply Current	I _{CC}			300	mA
Power Consumption			800	1050	mW
Data rate			10.3125		Gbps

Optical Characteristics

Parameter	Symbol	Min.	Typ.	Max	Unit	Notes
Transmitter						
Average Launch Power	P_o	-8.2		0.5	dBm	
Center wavelength	λ	1260		1360	nm	
Optical spectral width	$\sigma\lambda$			3	nm	
Extinction ratio	ER	3.5			dB	
Optical power OMA	P_{OMA}	-5.2			dBm	
Average launch power of OFF transmitted	P_{off}			-30	dBm	
RIN_{12OMA}	RIN			-128	dB/Hz	
Optical Return Loss Tolerance		12			dB	
Output eye	Compliant with IEEE802.3ae eye mask					
Receiver						
Center wavelength	λ	1260		1360	nm	
Average receiver overload (BER= 10^{-12})	P_{max}	0.5			dBm	
Receiver Reflectance	R_{rx}			-12	dB	
Receiver Sensitivity in OMA	Sens (OMA)			-12.6	dBm	
LOS	Assert	LOS_A	-30		dBm	
	Deassert	LOS_D		-12	dBm	
LOS Hysteresis	LOS_H	0.5		6	dB	

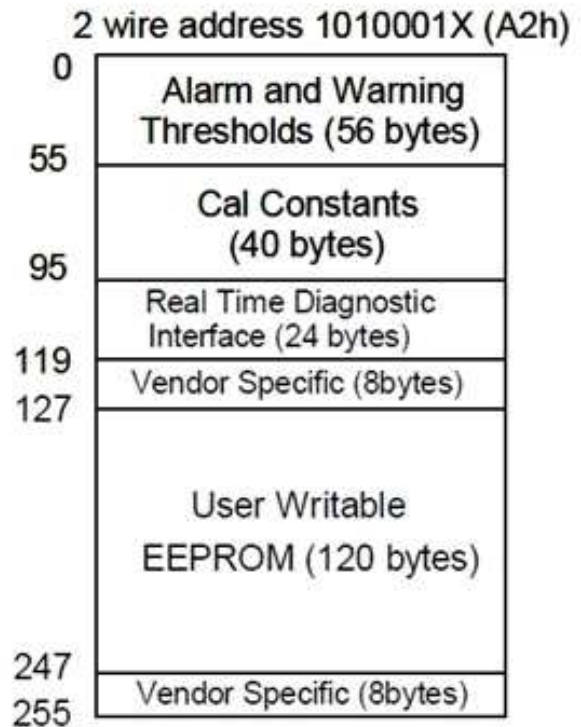
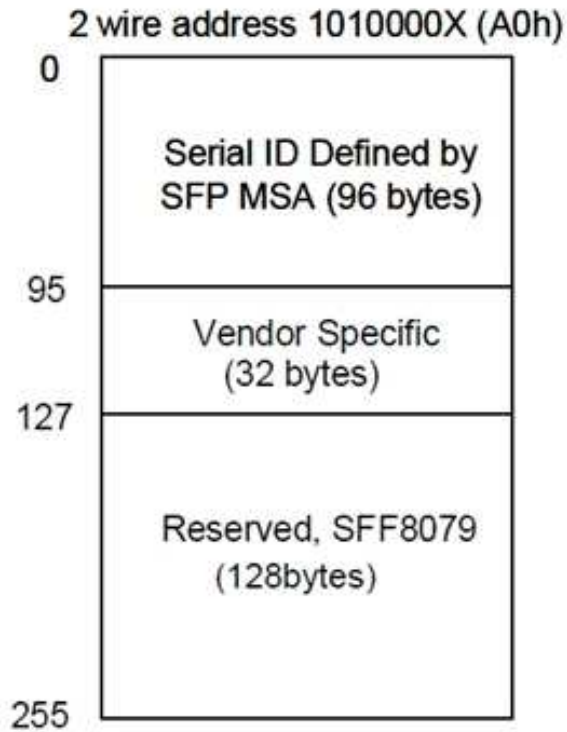
Electrical Characteristics

Parameter	Symbol	Min.	Typical	Max	Unit	Notes
Transmitter						
Transmitter Differential Input Voltage	V_{IN}	150		1000	mV	
Transmitter Disable (TX-Disable)	V_{IH}	2		V_{CC}	V	
	V_{IL}	Vee		$V_{EE}+0.8$		
Transmitter Disable Assert Time	T_{txdisa}			100	us	
Transmitter Disable Deassert Time	T_{txdisd}			2	ms	
Transmitter Fault (TX-Fault)	V_{FH}	2		V_{CC}	V	
	V_{FL}	Vee		$V_{EE}+0.8$		
Receiver						
Receiver Differential Output Voltage	V_O	300		1000	mV	
Loss of Signal (LOS)	V_{OH}	2.4		V_{CC}	V	
	V_{OL}	Vee		$V_{EE}+0.4$		
Rx Output Rise and Fall Time	T_r/T_f	30			ps	20% to 80%

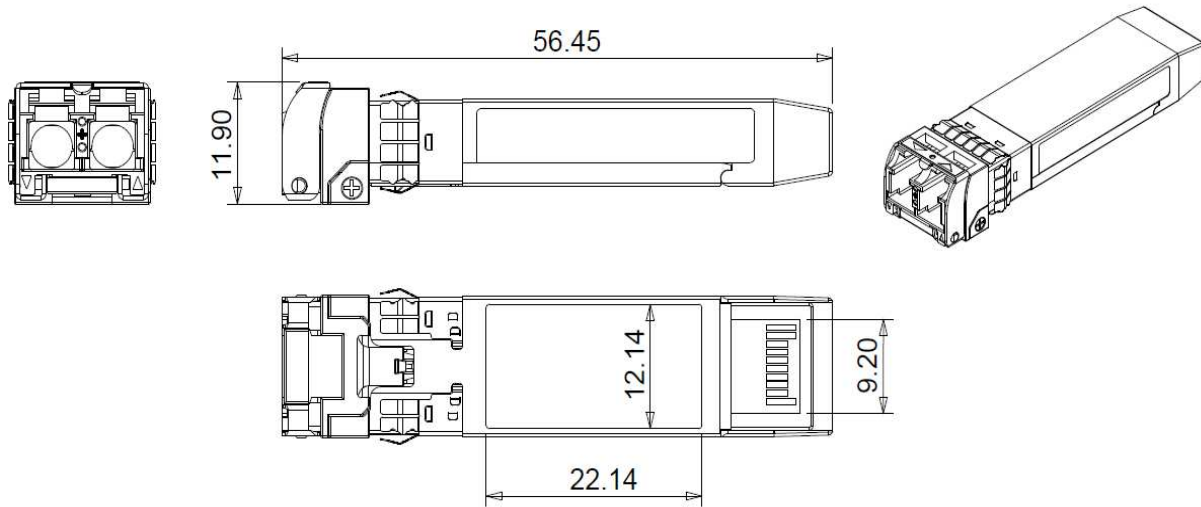
Control and Status I/O Timing Characteristics

Parameter	Symbol	Min	Max	Unit	Condition
TX Disable Assert Time	t_off		10	μs	Time from rising edge of TX Disable to when the optical output falls below 10% of nominal
TX Disable Negate Time	t_on		1	ms	Time from falling edge of TX Disable to when the modulated optical output rises above 90% of nominal
Time to initialize, including reset of TX_Fault	t_init		300	ms	From power on or negation of TX Fault using TX Disable
TX Fault Assert Time	t_fault		100	μs	Time from fault to TX fault on.
TX Disable to reset	t_reset	10		μs	Time TX Disable must be held high to reset TX_fault
LOS Assert Time	t_loss_on		100	μs	Time from LOS state to RX LOS assert
LOS Deassert Time	t_loss_off		100	μs	Time from non-LOS state to RX LOS deassert
Rate-Select Change Time	t_ratesel		10	μs	Time from rising or falling edge of Rate Select input until receiver bandwidth is in conformance with appropriate specification.
Serial ID Clock Rate	f_serial_clock		100	kHz	

Digital Diagnostic Memory Map



Mechanical (mm)



ESD

Normal ESD precautions are required during the handling of this module. This transceiver is shipped in ESD protective packaging. It should be removed from the packaging and handled only in an ESD protected environment.

LASER Safety

This is a Class 1 Laser Product according to IEC / EN 60825-1: 2014 (Third Edition). This product complies with 21 CFR 1040.10 and 1040.11 except for deviations pursuant to Laser Notice No. 50, dated (June 24, 2007)



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