

Specification

Small Form Factor

Duplex LC Receptacle – SFF

Optical Transceivers

STM-1 / OC-3 / 100BASE
155.52Mbit/s



Ordering Information

TSP - F2AA1 - D2G

Model Name	Voltage	Category	Device type	SD/LOS	Temperature	Media	Distance
TSP-F2AA1-D2G	3.3V	W/O DDMI	FP / PIN	LVPECL	-40 ~+85°C	Multi-Mode Fiber (50 μ m and 62.5μm)	2Km

Features

- ROHS Compliant
- Standard Small Form Factor Package – SFF MSA Compliant
- SONET/SDH Standard Compliant
- Fast Ethernet Standard Compliant
- Laser Class 1 Product – IEC / EN 60825-1 Compliant
- Standard Duplex LC Receptacle Optical Interface
- Single + 3.3 V Power Supply
- Differential LVPECL Data Input and Output
- LVPECL Signal Detect
- Low Power Consumption
- Conformal coating
- No connection between Case and PCB ground

Absolute Maximum Ratings

Parameter	Symbol	Min	Typ	Max	Unit
Storage temperature	TS	-40		85	°C
Supply voltage	VCC	0		4	V
Operating Relative Humidity	RH	5		95	%
Input voltage	VIN	0		VCC	V

Operating Conditions

Parameter	Symbol	Min	Typ	Max	Unit
Supply Voltage	VCC	3.1	3.3	3.5	V
Operating Case temperature (TSP-F2AA1-D2G)	Top	-40		85	°C
Current	Icc	-	-	130	mA
Soldering Temperature (10sec)	Tsold	-		260	°C

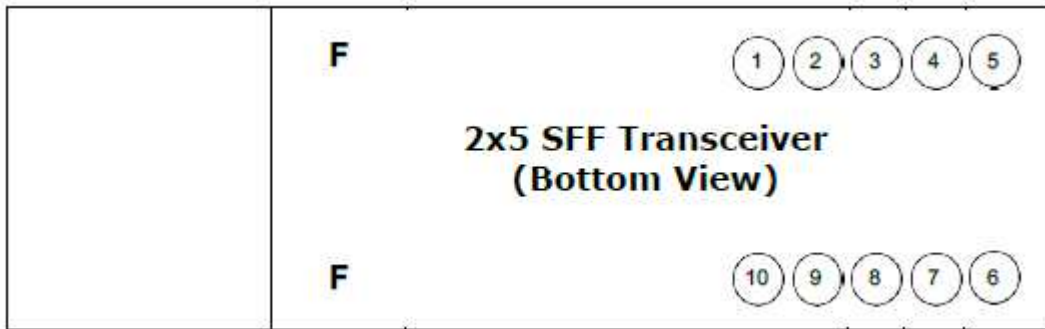
Transmitter Specifications (VCC = 3.1 ~ 3.5V ; Top = -40 ~ 85°C)

Parameter	Symbol	Min	Typ	Max	Unit
Optical Characteristics					
Output Optical Power	Pout	-19	--	-14	dBm
Extinction Ratio	ER	9	--	--	dB
Center Wavelength	λ	1260	1310	1360	nm
Spectral Width (RMS)	$\sigma\lambda$	--	--	7	nm
Rise/Fall time (10-90%)	Tr/ Tf	--	--	2	ns
Relative Intensity Noise	RIN	--	--	-120	dB/H
Output Eye	Compliant with IEEE 802.3z				
Max. Pout TX-DISABLE Asserted	POFF	--	--	-45	dBm
Electrical Characteristics					
Transmitter Data Input Voltage -High	VIH -Vcc	-1.1	--	-0.74	V
Transmitter Data Input Voltage - Low	VIL -Vcc	-2.0		-1.58	V
Tx_Disable_Input_High	VDISH	2.0	--	VCC+0.3	V
Tx_Disable_Input_Low	VDISL	0	--	0.8	V

Receiver Specifications (VCC = 3.1 ~ 3.5V ; Top = -40 ~ 85°C)

Parameter	Symbol	Min	Typ	Max	Unit
Optical Characteristics					
Optical Input Power-maximum	P _{max}	-3	--	--	dBm
Receiver Sensitivity (PRBS=2 ²³ -1 ; BER ≤ 10 ⁻¹⁰)	Sens	--	--	-32	dBm
Operating Center Wavelength	λ	1260	--	1610	nm
Signal Detect – Asserted	PSA	--	--	-32	dBm
Signal Detect – De-asserted	PSD	-48	--	--	dBm
Signal Detect – Hysteresis	PSH	0.5		6	dB
Electrical Characteristics					
Differential Output Voltage	V _{DIFF}	0.4	--	2.0	V
Signal Detect Output Voltage - High	V _{OH} -Vcc	-1.1	--	-0.74	V
Signal Detect Output Voltage - Low	V _{OL} -Vcc	-2.0		-1.58	V

Pin Definition and Descriptions



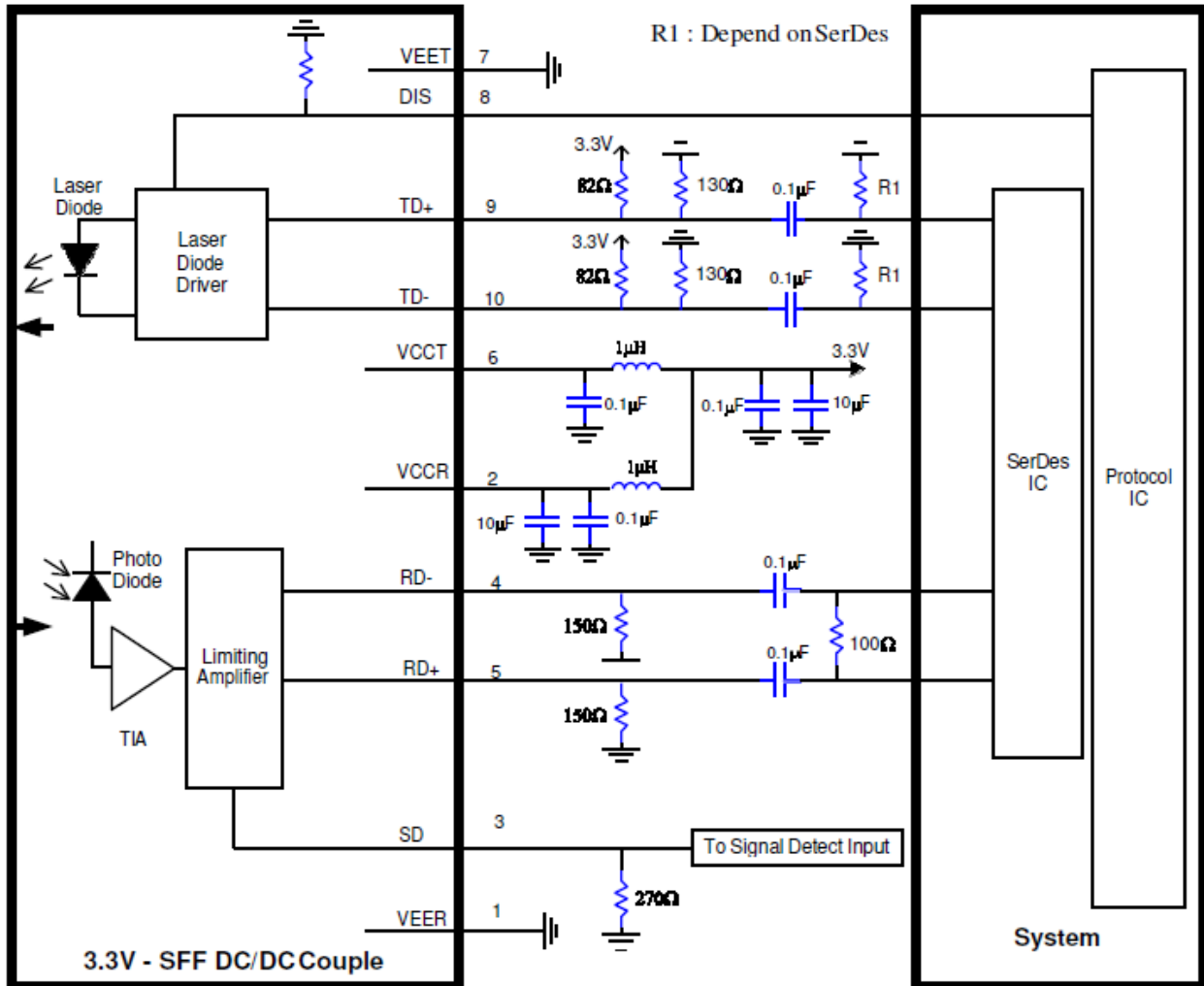
Bottom View

Pin	Name	Input-Output /Level	Description	Ref.
1	VEER	Input	Receiver ground	1
2	VCCR	Input	Receiver power supply	
3	SD	Output/LVPECL	Receiver signal detect. High signal indicates optical	
4	RD-	Output/LVPECL	Inverted receiver data output	
5	RD+	Output/LVPECL	Non-inverted receiver data output	
6	VCCT	Input	Transmitter power supply	
7	VEET	Input	Transmitter ground	1
8	DIS	Input/LVTTL	Transmitter Disable Control	
9	TD+	Input/LVPECL	Transmitter non-inverted data input	
10	TD-	Input/LVPECL	Transmitter inverted data input	

Note :

1. No connection between Case and PCB ground

Recommended Circuit Diagram



ESD

Normal ESD precautions are required during the handling of this module. This transceiver is shipped in ESD protective packaging. It should be removed from the packaging and handled only in an ESD protected environment.

Contact Information

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